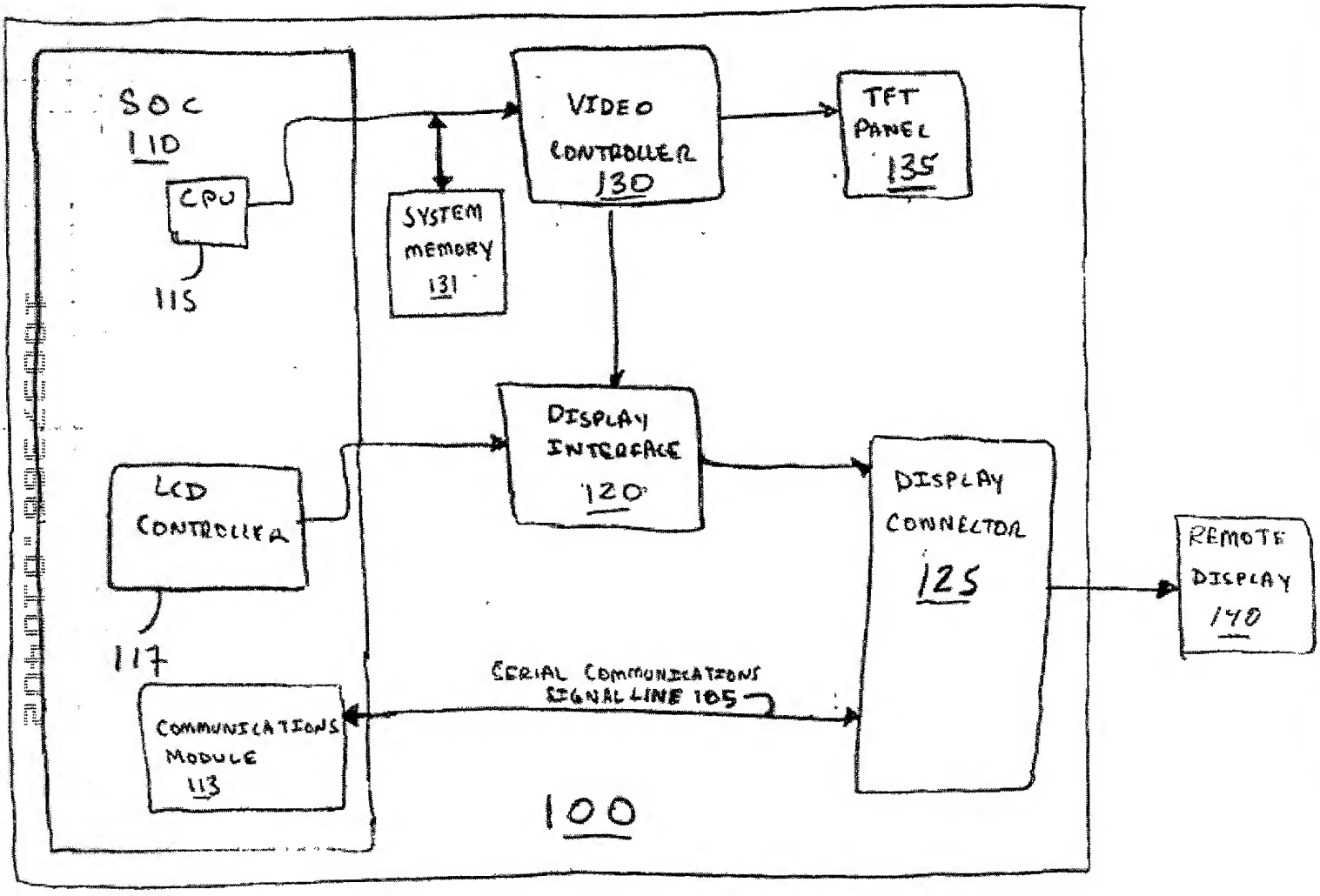
SOC
110
CPU
115
SYSTEM MEMORY
131
VIDEO CONTROLLER
130
TFT PANEL
135
DISPLAY INTERFACE
120
LCD CONTROLLER
117
DISPLAY CONNECTOR
125
REMOTE DISPLAY
140
SERIAL COMMUNICATIONS SIGNAL LINE 105
COMMUNICATIONS MODULE
113
100

FIG. 1

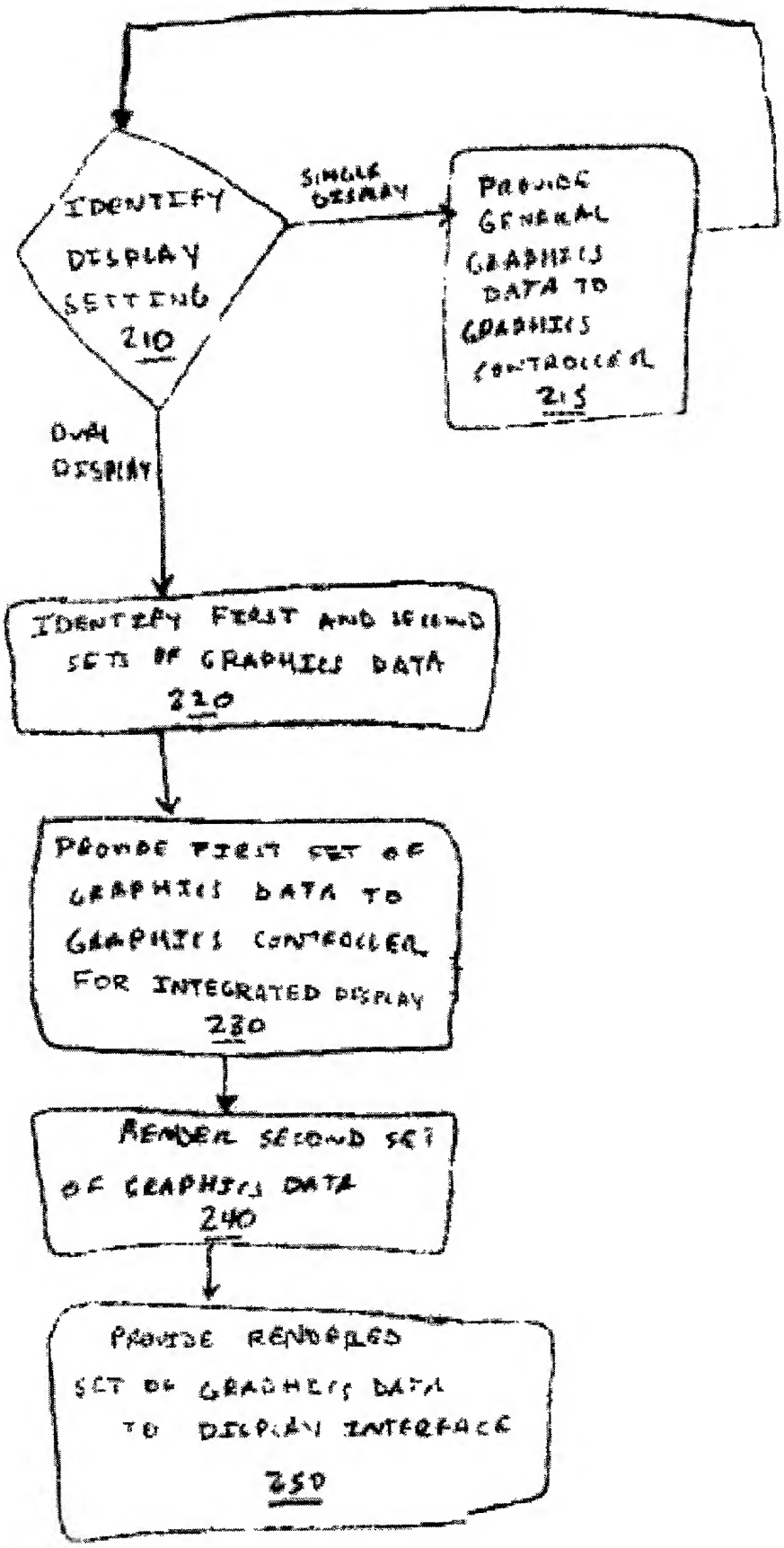

FIG. 2

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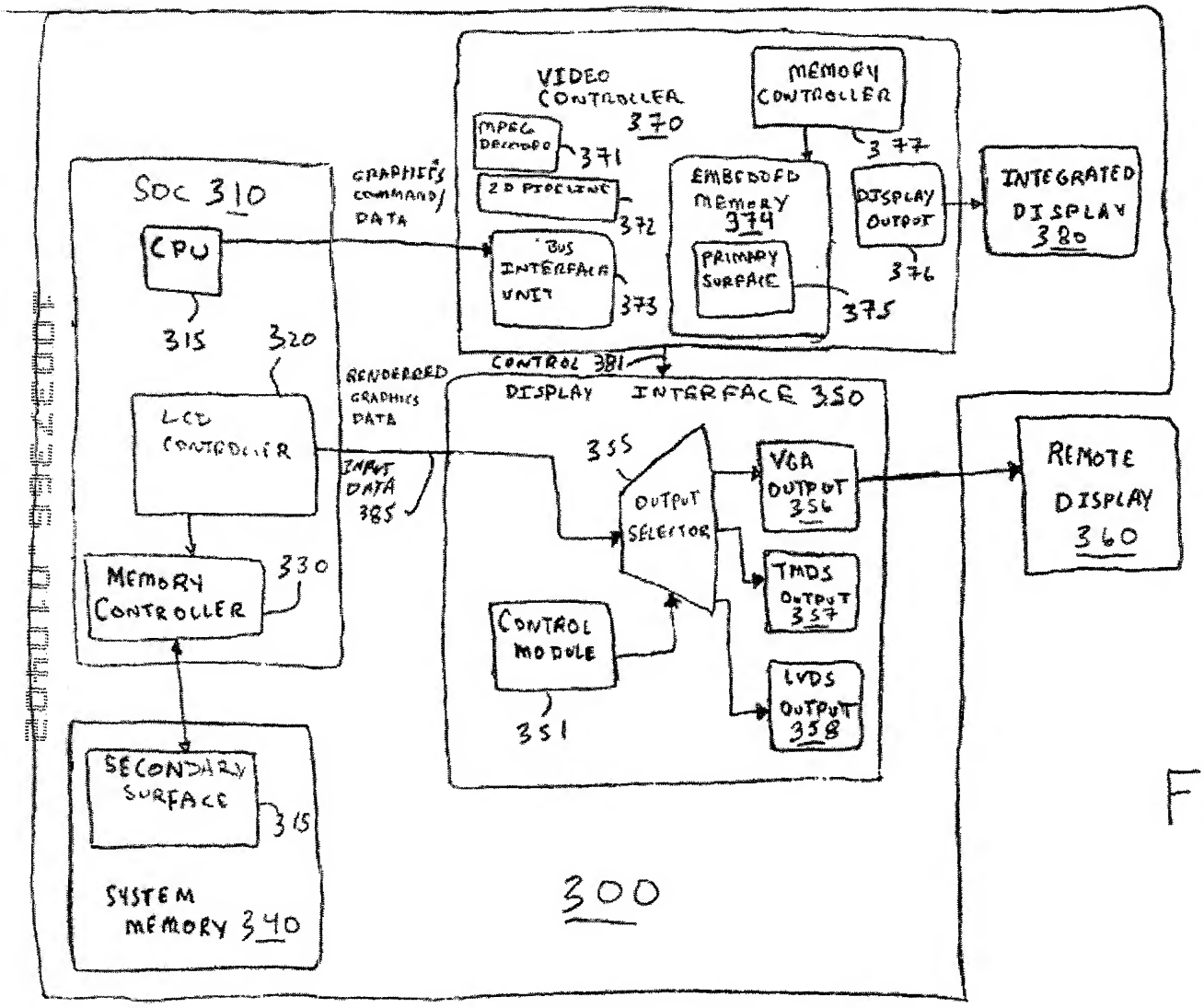
SOC 310
CPU
315
320
LCD CONTROLLER
MEMORY CONTROLLER
330
SECONDARY SURFACE
315
SYSTEM MEMORY 340
GRAPHICS COMMAND/ DATA
RENDERED GRAPHICS DATA
INPUT DATA 385
VIDEO CONTROLLER 370
MPEG DECODER
371
2D PROCESSOR
372
BUS INTERFACE UNIT
373
EMBEDDED MEMORY 374
PRIMARY SURFACE
375
MEMORY CONTROLLER
377
DISPLAY OUTPUT
376
INTEGRATED DISPLAY 380
CONTROL 381
DISPLAY INTERFACE 350
355
OUTPUT SELECTOR
VGA OUTPUT 356
TMDS OUTPUT 357
LVDS OUTPUT 358
CONTROL MODULE
351
REMOTE DISPLAY 360
300

FIG. 3

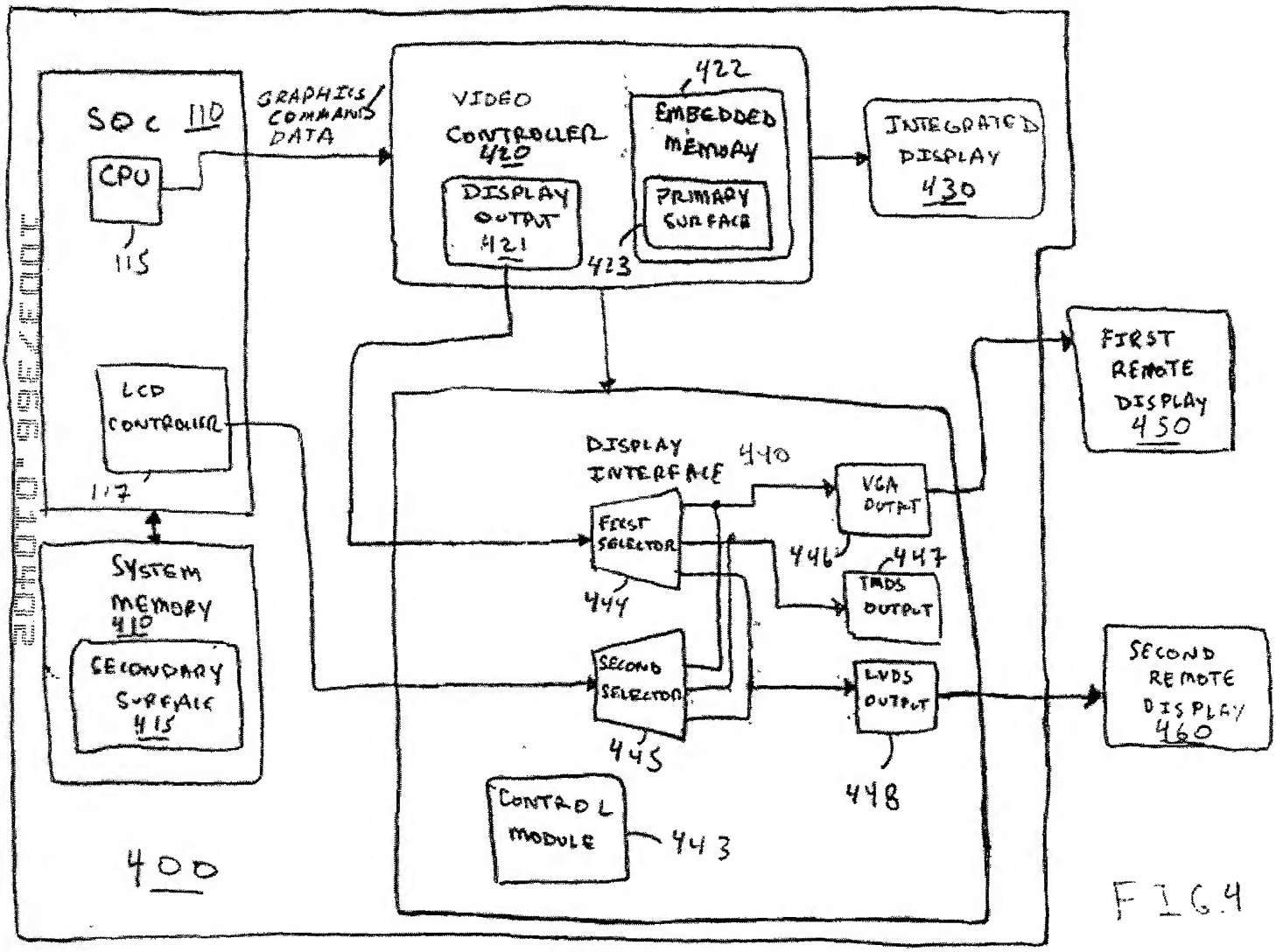
SOC 110
CPU
115
GRAPHICS/COMMANDS DATA
VIDEO CONTROLLER 420
DISPLAY OUTPUT 421
422
EMBEDDED MEMORY
PRIMARY SURFACE
423
INTEGRATED DISPLAY 430
FIRST REMOTE DISPLAY 450
LCD CONTROLLER
117
DISPLAY INTERFACE 440
FIRST SELECTOR
444
VGA OUTPUT
446
447
TMDS OUTPUT
SECOND SELECTOR
445
LVDS OUTPUT
448
SECOND REMOTE DISPLAY 460
SYSTEM MEMORY 410
SECONDARY SURFACE 415
CONTROL MODULE
443
400

FIG. 4